

PL7 PRO : test - [Configuration]

File Edit Utilities View Tools PLC Debug Options Window ?

TSX 57203 V6.2 ...

Number of predefined function blocks

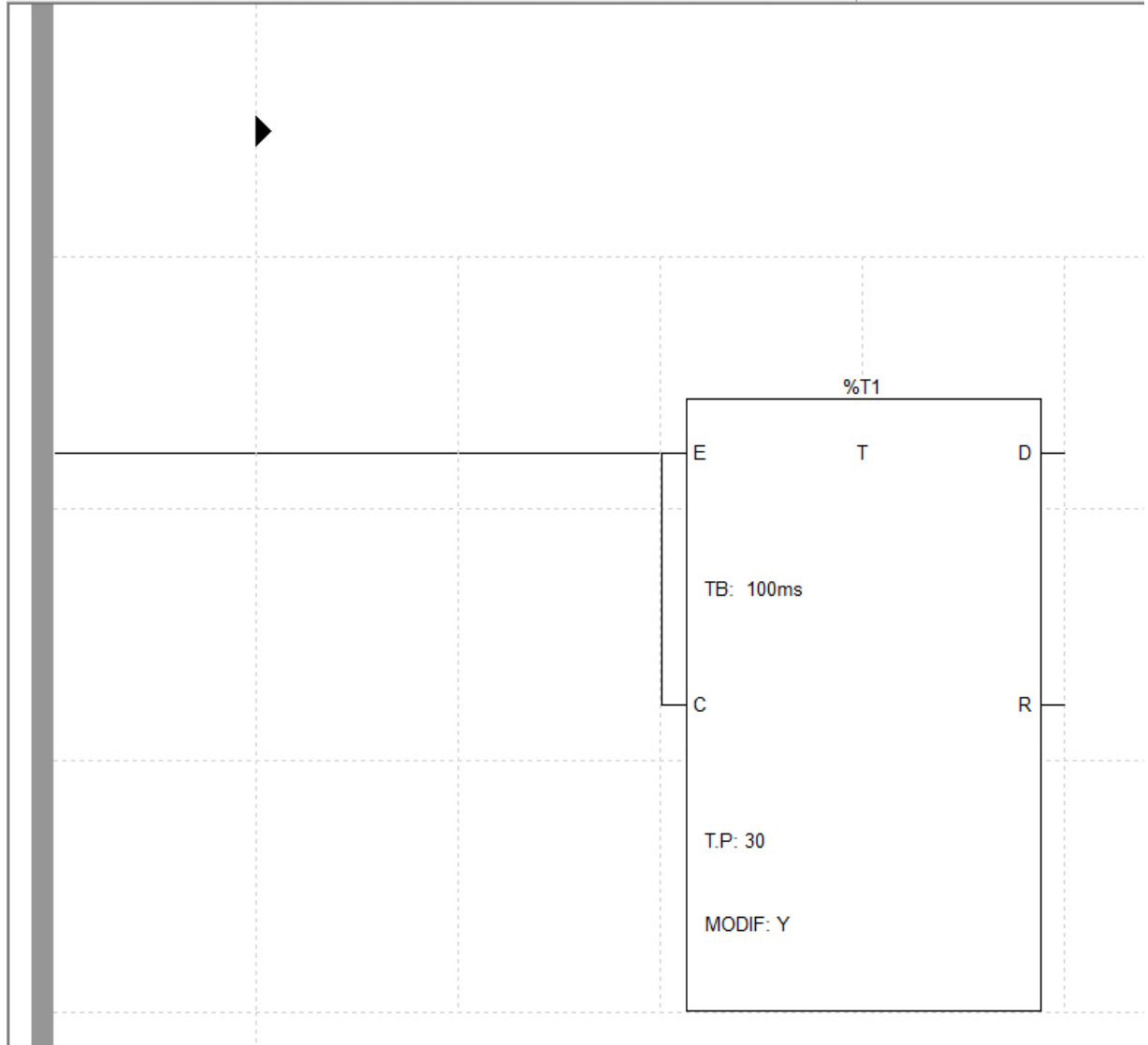
	Type	Number	Registers	Words
Timers	TM	64	%R0	16
Series 7 timers	T	64	%R1	16
Monostables	MN	32	%R2	16
Counters	C	64	%R3	16
Registers	R	4		
Drums	DR	8		

Size of global address fields

Bits: 512 Words: 1024 Constants: 256

PL7 PRO : test - [LD: MAST - Test]

File Edit Utilities View Tools PLC Debug Options Window ?



PL7 PRO : test - [Variables]

File Edit Utilities View Tools PLC Debug Options Window ?

Parameters PREDEFINED FE T Entry field

Address	Type	Symbol	Preset	TB	Req
%T0	T		9999	1 min	✓
%T1	T		30	100 ms	✓
%T2	T		9999	1 min	✓
%T3	T		9999	1 min	✓
%T4	T		9999	1 min	✓
%T5	T		9999	1 min	✓
%T6	T		9999	1 min	✓
%T7	T		9999	1 min	✓
%T8	T		9999	1 min	✓
%T9	T		9999	1 min	✓
%T10	T		9999	1 min	✓
%T11	T		9999	1 min	✓
%T12	T		9999	1 min	✓
%T13	T		9999	1 min	✓
%T14	T		9999	1 min	✓
%T15	T		9999	1 min	✓
%T16	T		9999	1 min	✓
%T17	T		9999	1 min	✓
%T18	T		9999	1 min	✓
%T19	T		9999	1 min	✓
%T20	T		9999	1 min	✓
%T21	T		9999	1 min	✓
%T22	T		9999	1 min	✓
%T23	T		9999	1 min	✓
%T24	T		9999	1 min	✓
%T25	T		9999	1 min	✓
%T26	T		9999	1 min	✓
%T27	T		9999	1 min	✓